

PCB Design & Layout (RJN SoC)

- Creation of schematics based on architecture requirements including library and BOM management.
- Interaction with multi-domains for requirement capture, reviews and approvals.
- Layout from placement to Gerber generation including library management.
- Board manufacturing and assembly including component procurement.
- Initial board bring up before passing it to PSV, FPGA & Software teams.



1 Lead
2 Engineers

Interfaces

- SDRAM, UART, SPI, Display, Camera



4 Months

Board

- Type : 4 Layer, FR4, 1.6mm
- Size : 275 x 250 mm
- Comp. Count : 360

Tools

- Cadence Allegro X 24.1, Viewmate