

# Post-Silicon Validation of x86 CPU

- Validation of x86 CPU – Client and Server Products
- IP Validation Focus
  - Core and Un-core (Cache Controller and related fabric)
  - CPU-PCH Interface
  - DDR
  - USB3, UFS, PCIe and CXL
  - VTD (Virtualization Technology for Directed I/O)
  - Graphics Subsystem
  - Camera Controller
  - RAS (Reliability, Availability and Serviceability)
  - HIOP (Host IO Processor IP)
  - Concurrency and Coherency Validation
  - Power Management (Core, Package, Device power states, Sleep states)
  - DTS accuracy, Prochot, Cat trip tests in Thermal Validation
  - Over Clocking
  - Triaging and first level of Debugging the defects
  - Verification of fixes and regression testing



75 Engineers  
2 Program Managers



Since 2018

