

Functional Validation of IP in ARM SoC

- Validation of Mobile SoC, Modem SoC, SSD Controller, Automotive Controller
- Processor Core and Subsystems
 - ARM Cortex M and ARM Cortex A Series
 - DSP Sub System
 - Interrupt Controller, Timer, Debug
 - Cache Memory, MMU
- System Control
 - Power, Clocks, Reset, WDT, NoC
- Peripheral Controllers
 - USB3, PCIe, Audio, I2C, SPI, UART
- Storage and Memory
 - SD, eMMC, UFS, DDR
- Security
 - SMMU, Memory Protection Unit, Crypto
- Multimedia
 - GPU, Display Controller, Camera Controller



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