

Electrical Validation of IPs

- Electrical Validation of IP interfaces
- Own Test Plan, Test Content, Execution and Debug
 - Develop Python based Automation
- IPs
 - PCIe Tx and Rx,
 - MIPI M-PHY, D-PHY, C-PHY
 - LPDDR,
 - USB3 and USB2
 - SERDES
 - HDMI
 - V-by-One
 - eDP
 - IO
 - PMIC



20 Engineers



Since 2014

