

DFT/Post-Si for Mobile Processor

Post silicon debug & pattern generation

Solution:

- Performed post-silicon debug/diagnosis interfacing with RTL, PnR & ATE teams
- Generated pre/post-silicon ATPG/MBIST patterns
- Supported power-on & silicon bring-up activities



1 Lead
5 Engineers

Results:

- Successful execution of ATPG/Simulations on multiple projects, 15+ partitions for each project
- Successful execution of Synth-DFT for multiple subsystems with TPI insertion



2+ Years (On going)

Tools: Siemens Tessent Tools , Genus, Xcelium

