

Automotive-SoC Subsystem Verification

- ADAS application
- Developed testbench components, scoreboard, test plan and test cases from scratch for the C2C block
- Verified the integration of the Ethernet PHY to C2C with both serial and parallel interface
- Comprehensive verification of the complete switch functionality of with various combinations of ports, interfaces and packet types
- Implemented Functional coverage model and code coverage and achieved 100% with waivers
- APB/AXI/Ethernet VIP



1 Lead
1 Program Manager
4 Engineers



8 Months

