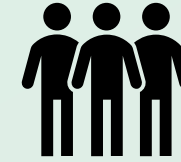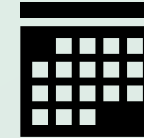# FPGA Prototyping Of AI Vision Platform

Deliver working FPGA Prototype of Complete SoC:

- Identify and select FPGA platform vendor
- Define deliverable for customer, software team and board design team
- Porting of ASIC IP for selected FPGA family, design partitioning, design verification.
- Synthesis, Place and Route, Timing Closure
- FPGA Bitstream and platform bring-up
- Linux OS and Application software bring-up for pre-silicon validation

Tools: Synplify, Vivado, Verilog, C, Perl

1 Project Manager
1 Lead Engineer
7 FPGA Engineers

12 months

T&M with ownership of deliverables