

# Memory layout 7nm

**Customer** : The customer is a tier-1 company.

**Project** : Custom memory layout development on 7nm including characterization for high speed application.

## **Mirafrá's Responsibility** :

- Develop leaf cells for Custom memory.
- Building Sense amplifiers and other IO components.
- Building variety of memory structures.
- Extraction for checking speed and performance.
- Sign off with Characterization.

**Tools used** : Virtuoso (Cadence), Caliber (Mentor)

**Engagement Model** : A team of 4 people for 6 months onsite based on T&M model.