

DC-DC Converters Layout (180nm)

Customer : The customer is a tier-1 company.

Project : The DC-DC was designed in state of art 180nm process.

Mirafra's Responsibility : Mirafra was involved in completion of full ASIC level layout.

- Floorplan of complete core blocks like Chargepump , Drivers, LDO delivery.
- Contribution to all core blocks layout design and top level routing.
- Maintaining robust signal widths for the entire signal path.
- Context dependency layout to take care of coupling, matching , EMIR etc.
- Maintaining required RV vias from the Powerfets to Bump.

Challenges: Managing high current the current consumption of 2A

Tools used : Virtuoso (Cadence), Assura-PVS

Engagement Model : Delivered project in period of 3 months with team of 5 people.