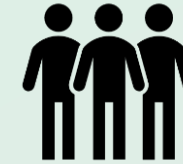


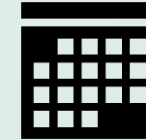
Physical Verification & EMIR of Hard Macros, Processors & SOCs

- **End to End Physical Verification for multiple Hard Macro projects**
- **Develop Automated Flows and Fixers**
- **Perform IR Drop and EM analysis for Hard Macro blocks**
- **Completed as many as 5 tapeouts in year per engineer for over 5 years**

Tools: DC, Genus, Primetime



1 Lead Engineer
8-10 Engineers



5 years



ODC with ownership of deliverables