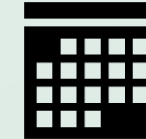


# TFM for CXL-based memory Expansion SOC

- **Ownership of implementing the TFM for various front- end tools.**
- **Working closely with the designer to fix the design issues that appears during the TFM flow**
- **Integration of IP/IP sub-system to SoC**
- **Debugging flow issues at the SoC level**
- **Lint, CDC, RTL compilation**
- **Setting up the tool environment**
- **Cleaning the violations**
- **UPF validation**
- **SDC Constraint validation by Fishtail**
- **Fix violations**



**1 Lead – 12+ Years**  
**2 Engineers – 7+ years**  
**4 Engineers – 2+ Years**  
**2 Buffer Engineers**



**Ongoing since 36 months**



**T&M with ownership of deliverables**