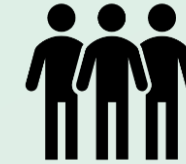
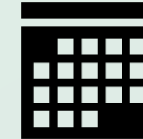


WIFI Controller Sub System

- **Relatively complex IP, It has own Processor (RISCV/Cortex M) , NOC infra , 2 WIFI MAC's (UMAC, LMAC) PHY, MSIP.**
- **Handled 4 variants of the IP during the period of 2 years. These IP's goes into the SoC's which will be used in 4G Mobile devices**
- **Branching off from tape out completed project to create another version of IP.**
- **Adding/Removing IP features**
- **Updating NoC infrastructure, Modifying data path for new architecture.**
- **Ownership for implementing Qchecks (Quality Checks) which includes, Lint, CDC, Synth_elab, Unified flow , VCS, MTS, Questa, Novas compilation, Integrating power flows (VCLP/UPF) into Unified Flow.**
- **Qchecks env/setup bringup**
- **Implement scripts for redundant waiver cleanup**
- **Interacting with various sub IP owners regarding the Lint, CDC, timing violations originating in their IP's and get it fixed.**



1 Lead – 12+ Years
1 Engineer



24 months



All Qcheck flows and results, Scripts