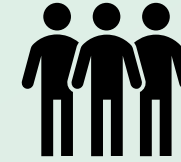
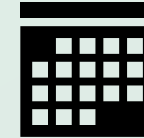


Functional Validation of IP in ARM SoC

- Validated different modules for multiple SoC projects
- Own Test Plan, Test Content, Execution and Debug
 - Develop C based Test Content and HAL
- Processor Core and Subsystems
 - ARM Cortex M and ARM Cortex A Series
 - DSP Sub System
 - Interrupt Controller, Timer, Debug
 - Cache Memory, MMU
- System Control - Power, Clocks, Reset, WDT, NoC
- Peripheral Controllers - USB3, PCIe, Audio, I2C, SPI, UART
- Storage and Memory - SD, eMMC, UFS, DDR
- Security - SMMU, Memory Protection Unit, Crypto
- Multimedia - GPU, Display Controller, Camera Controller



Multiple PSV
Engineers working
across different
projects



To be Updated



T&M with
ownership of
deliverables