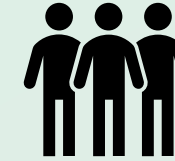
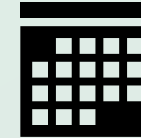


# Functional Validation of multiple x86 Server CPU

- Latest CPUs in Server Product line since 2020
- Automation Environment Setup and maintenance
- Porting and Development of Tools for Validation
- Functional Validation of following IP on Emulation Platform and Validation Platform
  - PCIe
  - Uncore (Cache Controller and related fabric)
  - DDR/Memory
  - Power Management (Core, Package, Device power states, Sleep states etc)
  - Concurrency Testing
- Coherency Validation



36 PSV Engineers



30 months



Outcome Based  
Model