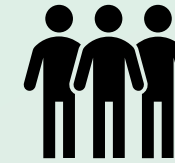
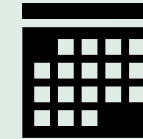


Functional Validation of multiple x86 Client CPU

- Latest CPUs in Client Product line since 2018
- Automation Environment Setup and maintenance
- Functional Validation of following IP on Emulation Platform and Validation Platform
 - Camera Controller
 - CPU-PCH Interface
 - Uncore (Cache Controller and related fabric)
 - USB 3, UFS, PCIe
 - Power Management (Core, Package, Device power states, Sleep states etc)
 - Over Clocking
 - Concurrency Testing
 - OS bring up (Chrome OS, Windows 10 and Linux based validation OS)
 - Power Validation



26 PSV Engineers



60 months



Outcome Based
Model