

Design Verification – Mobile SOC

- **Top level verification using C**
- **Block level verification using SV and C**
- **Creation of test-plans**
- **Development of verification components**
- **Test-case development**
- **Coverage analysis and closure**
- **Regression failure analysis and triage**

2 Quad-core, 2 Dual-core ARM cortex processors

Audio, Video, Display sub-system

High speed peripherals like USB, NAND

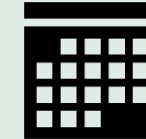
Power reset clock management



1 Project Manager

5 Leads

25 Engineers



12 months



ODC with ownership of deliverables