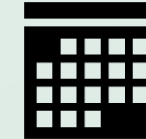# CNN (Convolutional Neural Network) IP Verification

- **Ownership of functional verification with of several blocks within the IP Subsystem using C, C model, System Verilog, UVM, Constraint random methodology**

- **Architecting whole portions of the test bench and all blocks from scratch**

- **Integration of various blocks and functional verification with the help of API(C Model) of the integrated system**

- **Developed comprehensive test plans and functional coverage plans and performed code coverage**

4 DV Engineers

12 months

Lead-led execution at Client location