

Bio-Medical DNA sequencing platform

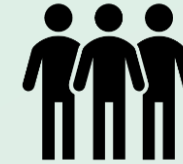
- **IP and Sub system verification**
- **Development of Verification Environment**
- **Integration of third-party verification IP's**
 - **PCIE 3.0, SPI, I2C**
- **Verification signoff through functional, assertion and code coverage**
- **SV, UVM, C, Assembly**

ARM cortex M processor

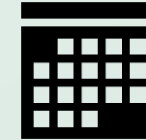
LPDDR5, Micron memory models

5 SPI Masters, 2 I2C Masters, PCIe Endpoint

DMA Engine



1 Project Manager
5 Engineers



Ongoing since 36 months



ODC with ownership
of deliverables