

SSD Controller Multiple Subsystem Verification

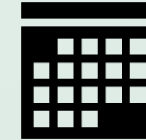
ASIC verification of 4 Subsystems

- **Subsystems to be verified at SOC level : CPU, Debug, Always ON, Interconnect, Security**
- **Subsystem level verification as required**
- **Verify RTL for ASIC, FPGA and Emulation builds**
- **Gate level simulation and Power Aware GLS**
- **Test plan**
- **Development of testbench components as per need**
- **Assertions + coverage closure**
- **Post Silicon support**

Tools: C/Assembly, SV, UVM



**2 DV Leads
7 Engineers**



16 months



**ODC with ownership
of deliverables**