

# Case Study 3: Virtual Prototype development and validation for New chip design

## Business Challenge

To Improve model performance and new functional module development



### Requirement

- VP (Virtual Prototype ) developments and validation for New chip design
- Fix bugs and minor enhancement
- Functional testing in VP



### Technology & Tools

- System C
- Virtualizer studio ( Synopsis )
- Robo Framwork
- Inhouse Specific Tools



### Value add by Mirafra

- Reduced total cost of production for new chips with virtual prototyping
- Improved model performance