FUNCTIONAL AND FORMAL VERIFICATION OF WIRELESS 3G MODEM IP USING SV

The Customer:
Customer is a major mobile maker.

The Application:
3G modem IP with applications in high-end smart phones.

Mirafra's Responsibility:

- Definition of verification strategy using multiple verification techniques including, but not limited to, constrained random verification, formal verification and C/VHDL based verification.
- Definition of verification plan and sign-off criteria.
- Architecting, designing and development of SV/VMM based environment for full IP verification with and without ARM processors in DUT.
- Development of test cases and analysis of results.
- Driving verification to closure by achieving desired sign-off metrics.
- Identification of features suitable for formal verification.
- Definition of formal properties and usage of IFV to verify the correctness.
- Conversion of PSL based formal properties into SV assertions for dynamic simulation coverage.

Engagement Model:
A team of 15 people for 12 months onsite based on T&M