

## ARM BASED PROCESSOR SUBSYSTEM VERIFICATION

### **The Customer:**

Global leader in semiconductor solutions for wired and wireless communications.

### **The Application:**

A range of ARMV7, ARMV8, application and real-time architecture based processor subsystems that are designed to power a number of switch chips across the performance and application spectrum. With CPU cores varying from Cortex-R5 to Cortex-A53, and integrated communication IPs like PCIe, USB, Ethernet MAC, SPI, JTAG etc., these subsystems are designed to be powerful and versatile, while being efficient.

### **Mirafra's Responsibility:**

- Planning and creation of the functional verification infrastructure from ground up.
- Creation and ownership of tests to check integration of ARM Debug Interface and processor debug capabilities.
- Ownership of test bench components that verify security architecture.
- Silicon Validation activities using ARM's RealView Debugger.
- Assist several teams across the globe to debug their integration issues/chip issues related to this subsystem.
- Verification closure for complex industry standard protocol IPs like PCIe, USB.

### **Engagement Model:**

A team of two engineers for twelve months onsite based